

**APPENDIX A**  
**VERSION OF SPECIFICATION AND CLAIMS**  
**WITH MARKINGS TO SHOW CHANGES MADE**

**IN THE CLAIMS**

Claims 2-23 are newly added, so no marked-up version is shown for these claims.

1. (Once Amended) A computer system comprising:

a controller to generate a power state status signal [indicating the] to indicate  
a power state[s] of a first plurality of components of the computer system;  
and  
a voltage regulator [coupled] to supply a voltage level to the first plurality of  
components and to increase [an output] the voltage level to the first  
plurality of components when the first plurality of components enters a  
sleep state, as indicated by the power state status signal.